

Product data sheet

1. General description

NPN low V_{CEsat} transistor in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

PNP complement: PBSS5240T

2. Features and benefits

- · Low collector-emitter saturation voltage
- · High current capability
- · Improved device reliability due to reduced heat generation
- AEC-Q101 qualified

3. Applications

- Supply line switching circuits
- Battery management applications
- DC/DC converter applications
- · Strobe flash units
- Heavy duty battery powered equipment (motor and lamp drivers)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base	-	-	40	٧
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	-	3	Α
R _{CEsat}	collector-emitter saturation resistance	I_C = 500 mA; I_B = 50 mA; pulsed; $t_p \le$ 300 μs; $\delta \le 0.02$; T_{amb} = 25 °C	-	140	200	mΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base]3	С
2	E	emitter		j
3	С	collector		В —
			SOT23	 E sym123



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6. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
PBSS4240T		plastic, surface-mounted package; 3 terminals; 1.9 mm pitch; 2.9 mm x 1.3 mm x 1 mm body	SOT23				

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS4240T	ZE%

^{[1] % =} placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	40	V
V_{CEO}	collector-emitter voltage	open base		-	40	V
V_{EBO}	emitter-base voltage	open collector		-	5	V
Ic	collector current			-	2	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	3	А
I _{BM}	peak base current			-	300	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
			[2]	-	480	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1]	-	-	417	K/W
	junction to ambient		[2]	-	-	260	K/W

Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C	-	-	100	nA
	current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C	-	-	50	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 4 V; I _C = 0 A; T _{amb} = 25 °C	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 100 mA; T _{amb} = 25 °C	350	470	-	
		V _{CE} = 2 V; I _C = 500 mA; T _{amb} = 25 °C	300	450	-	
		V _{CE} = 2 V; I _C = 1 A; T _{amb} = 25 °C	300	420	-	
		V _{CE} = 2 V; I _C = 2 A; T _{amb} = 25 °C	150	250	-	
V _{CEsat}	collector-emitter	I _C = 100 mA; I _B = 1 mA; T _{amb} = 25 °C	-	45	70	mV
	saturation voltage	I _C = 500 mA; I _B = 50 mA; T _{amb} = 25 °C	-	70	100	mV
		I _C = 750 mA; I _B = 15 mA; T _{amb} = 25 °C	-	120	180	mV
		I_C = 1 A; I_B = 50 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	130	180	mV
		I_C = 2 A; I_B = 200 mA; pulsed; t_p ≤ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	240	320	mV
R _{CEsat}	collector-emitter saturation resistance	I_C = 500 mA; I_B = 50 mA; pulsed; $t_p \le$ 300 μs; $\delta \le$ 0.02; T_{amb} = 25 °C	-	140	200	mΩ
V _{BEsat}	base-emitter saturation voltage	I_C = 2 A; I_B = 200 mA; pulsed; $t_p \le$ 300 μs; δ ≤ 0.02; T_{amb} = 25 °C	-	-	1.1	V
V_{BEon}	base-emitter turn-on voltage	$V_{CE} = 2 \text{ V}; I_{C} = 100 \text{ mA}; T_{amb} = 25 \text{ °C}$	-	-	0.75	V
f _T	transition frequency	V_{CE} = 10 V; I_{C} = 100 mA; f = 100 MHz; T_{amb} = 25 °C	100	230	-	MHz
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; $ $T_{amb} = 25 ^{\circ}\text{C}$	-	15	20	pF

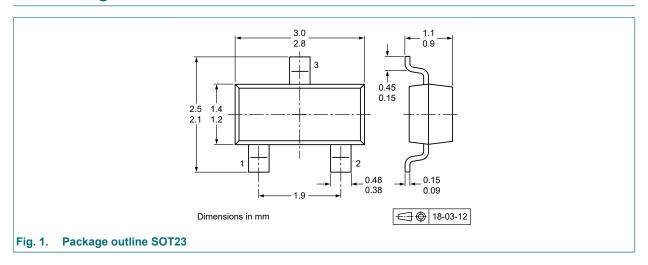
11. Test information

Quality information

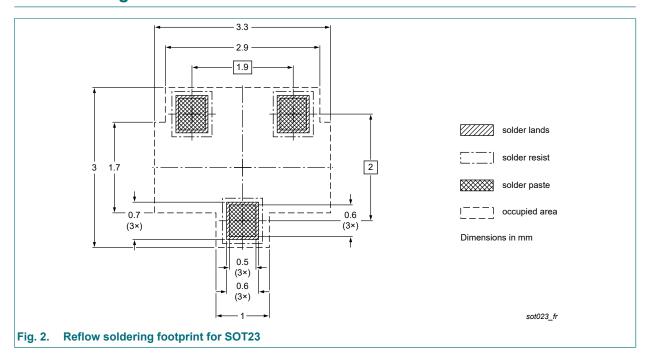
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

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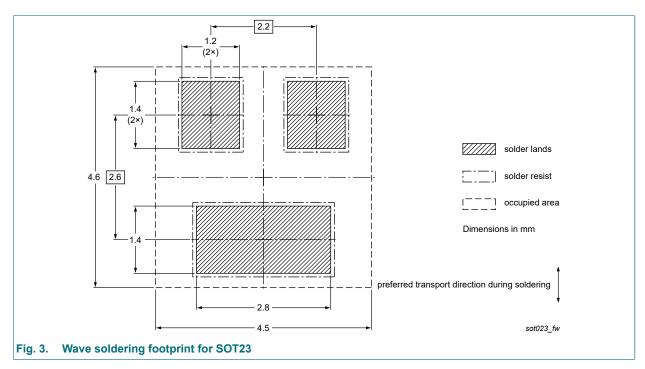
12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history

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Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PBSS4240T v.3	20220513	Product data sheet	-	PBSS4240T v.2		
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. 					
PBSS4240T v.2	20040109	Product data sheet	-	PBSS4240T v.1		
PBSS4240T v.1	20010713	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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